

8.2.2. Wiring the Master Operator

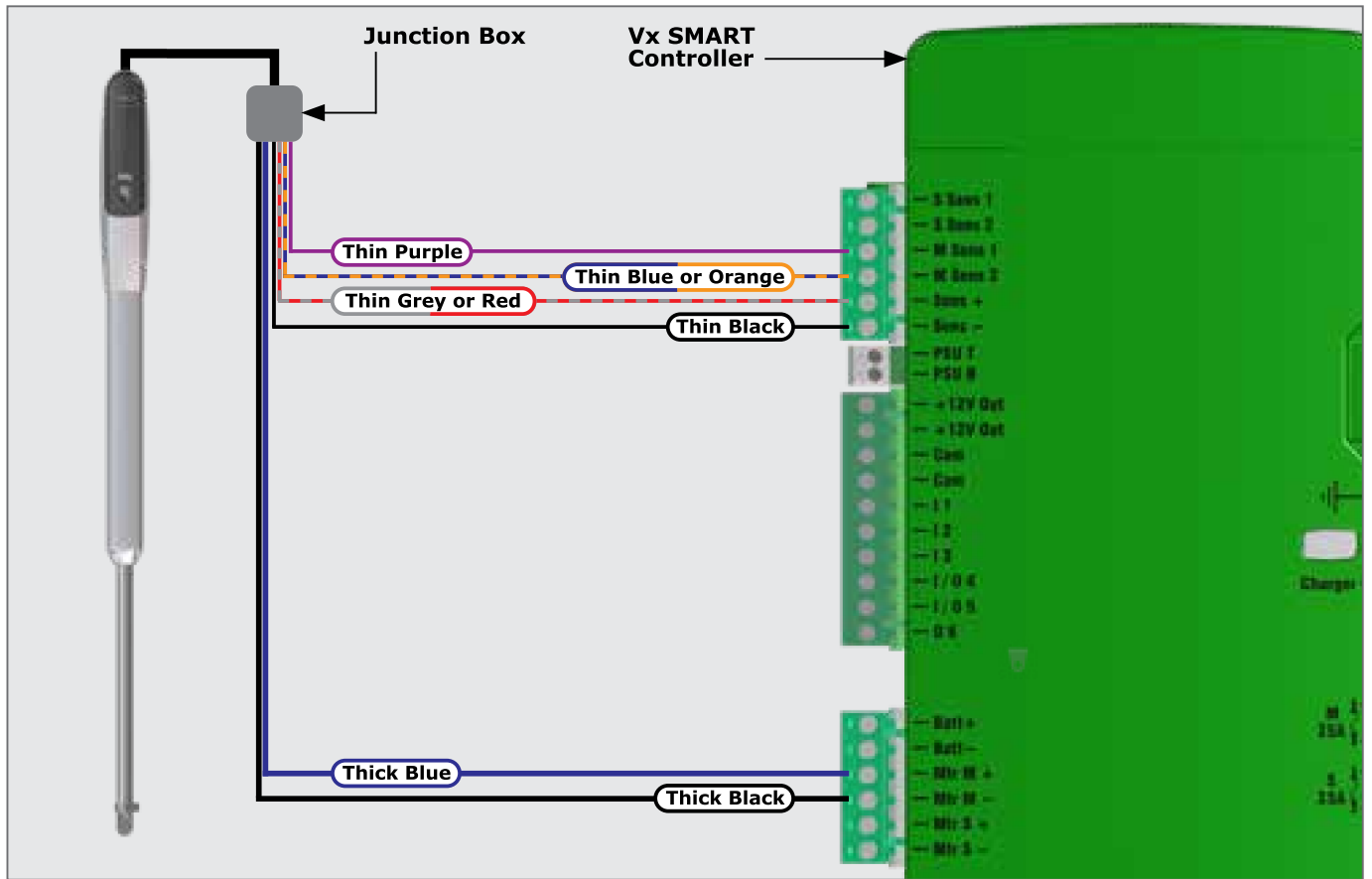


FIGURE 41

8.2.3. Wiring the Slave Operator

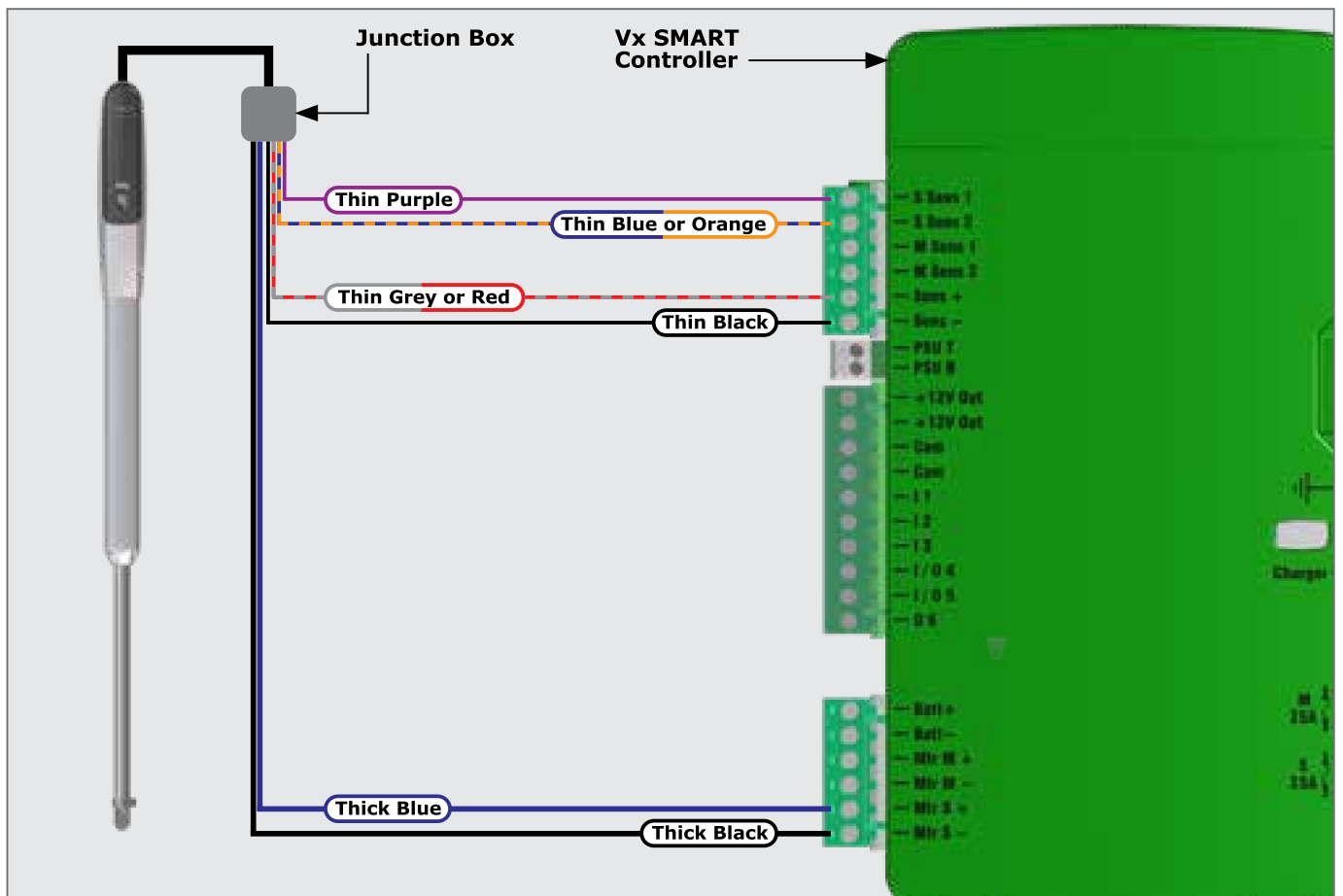


FIGURE 42